

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: 2814

Serial No.: 09/855,590

he Application of: FUKASAWA, Shinji

Examiner: QUACH, Tuan N.

P.T.O. Confirmation No.: 1417

Filed: May 15, 2001

For.

SEMICONDUCTOR DEVICE HAVING A MULTIPLE LAYER WIRING STRUCTURE, WIRING METHOD, WIRING DEVICE, AND RECORDING

MEDIUM

PETITION FOR EXTENSION OF TIME

Commissioner for Patents Washington, D.C. 20231

Date: August 30, 2002

TECHNOLOGY CENTER 2800

Sir:

Applicants petition the Commissioner for Patents to extend the time for response to the Office Action dated May 3, 2002 for one month from August 3, 2002 to September 3, 2002.

Attached please find a check in the amount of \$278.00 to cover the cost of the extension (\$110.00) and to cover the cost of the additional claims fees (\$168.00) for a Large entity. In the event that any additional fees are due in connection with this paper, please charge our Deposit Account No. 01-2340. Two copies of this paper are enclosed herewith.

Respectfully submitted,

ARMSTRONG, WESTERMAN & HATTORI, LLP

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illiam L. Brooks Attorney for Applicant

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WLB/mla

Atty. Docket No. 010623 Suite 1000, 1725 K Street, N.W. Washington, D.C. 20006 (202) 659-2930

PATENT TRADEMARK OFFICE